

**Notice of Allowability**

Application No.

10/763,561

Examiner

Linh V. Nguyen

Applicant(s)

MENKUS ET AL.

Art Unit

2819

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 2/21/07.
2. ☒ The allowed claim(s) is/are 1 - 3, 6 - 12, 14 - 20, and 22 - 28.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

### **DETAILED ACTION**

1. This office action is in response to communication filed on 2/21/07. Claims 20, and 23 – 27 have been amended. Claims 4, 5, 13 and 21 have been canceled. Claims 1 – 3, 6 – 12, 14 – 20, and 22 – 28 are remaining in this application.

### ***Allowable Subject Matter***

2. Claims 1 – 3, 6 – 12, 14 – 20, and 22 – 28 are allowed.

The following is an examiner's statement of reasons for allowance:

With respect to claim 1, in addition to other elements in the claim, prior art considered individual or combination does not teach or suggest a circuit for reducing the current density on a bus comprising: a second amplifier circuit of the plurality of circuits, wherein the second amplifier circuit is configured to provide a second output current for the bus; and a second current source circuit that is configured to provide a second local current at an output of the second amplifier circuit such that at least a portion of the second output current is prevented from being carried on the bus

With respect to claim 11, in addition to other elements in the claim, prior art considered individual or combination does not teach or suggest a circuit comprising: the first current source circuit is arranged to provide a first local current at the output of the first transconductance circuit such that a maximum magnitude of current density is decreased on at least the first bus of the plurality buses; and another current source circuit that is coupled to an output of another transconductance circuit in the of the transconductance circuits.

With respect to claim 15, in addition to other elements in the claim, prior art considered individual or combination does not teach or suggest amplifier circuit comprising: a plurality of current source circuits that includes the first current source circuit, wherein each of the plurality of transconductance circuits has an output coupled to a separate one of the plurality of current source circuits, wherein each of the plurality of load circuits includes a load current source, and wherein each of the plurality of current source circuits (shares a bias line in common with one of the load current sources.

With respect to claim 20, in addition to other elements in the claim, prior art considered individual or combination does not teach a circuit for decreasing current density on a bus comprising: means for amplifying a second input signal to provided a second current to the bus; means for canceling output at least a portion the second current with the second local current such that a maximum magnitude of a total current on the bus is reduced.

With respect to claim 26, in addition to other elements in the claim, prior art considered individual or combination does not teach or suggest: a circuit for reducing the current density, comprising: a second current source circuit; wherein the output of second current source circuit is connected to the bus, and wherein the second current circuit is arranged to provide a second local current at an output of the second current source circuit such that at least a portion of the second output current is prevent from being carried on the bus.

With respect to claim 28, in addition to other elements in the claim, prior art considered individual or combination does not teach or suggest: a circuit for reducing the current density, comprising: a third current source circuit that is arranged to provide a third local current at the first output of the third amplifier circuit such that at least a portion of the first half of the third differential current is prevented from being carried on the third bus; and a fourth current source circuit that is arranged to provide a fourth local current at the second output of the fourth amplifier circuit such that at least a portion of the second half of the third differential current is prevented from being carried on the fourth bus.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Contact Information***

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh Van Nguyen whose telephone number is (571) 272-1810. The examiner can normally be reached from 8:30 – 5:00 Monday-Friday. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mr. Rexford Barnie can be reached at (571) 272-7492. The fax phone numbers for the organization where this application or proceeding is assigned are (571-273-8300) for regular communications and (571-273-8300) for After Final

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communications.

LINH NGUYEN  
PRIMARY EXAMINER

3/1/07

Linh Van Nguyen

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A handwritten signature in black ink, appearing to read 'Linh Van Nguyen', written in a cursive style.